

Fig. 1

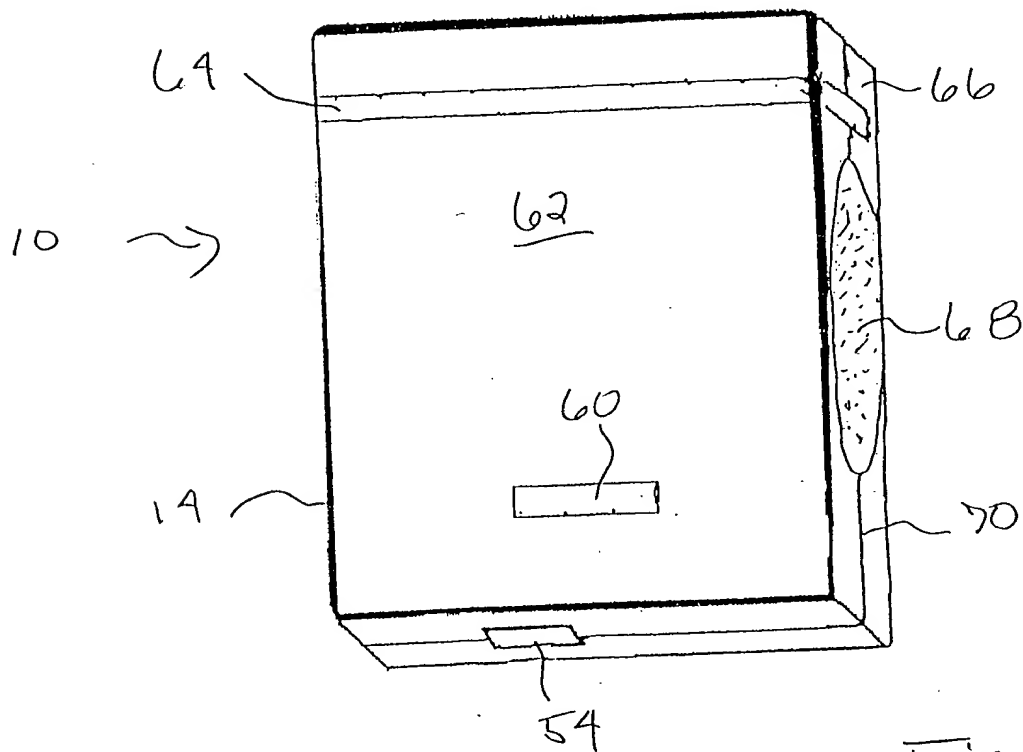


Fig. 2

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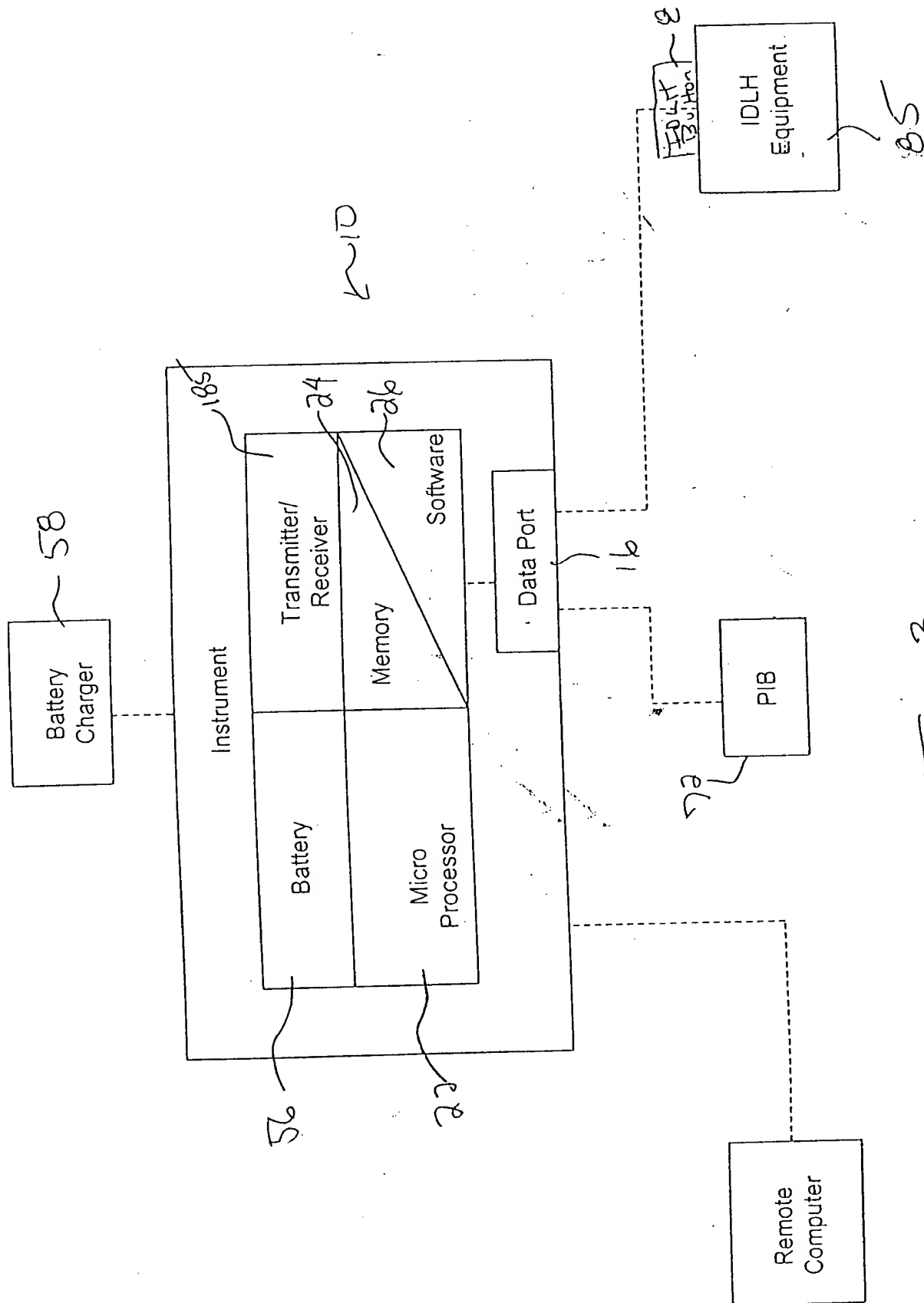


Fig. 3

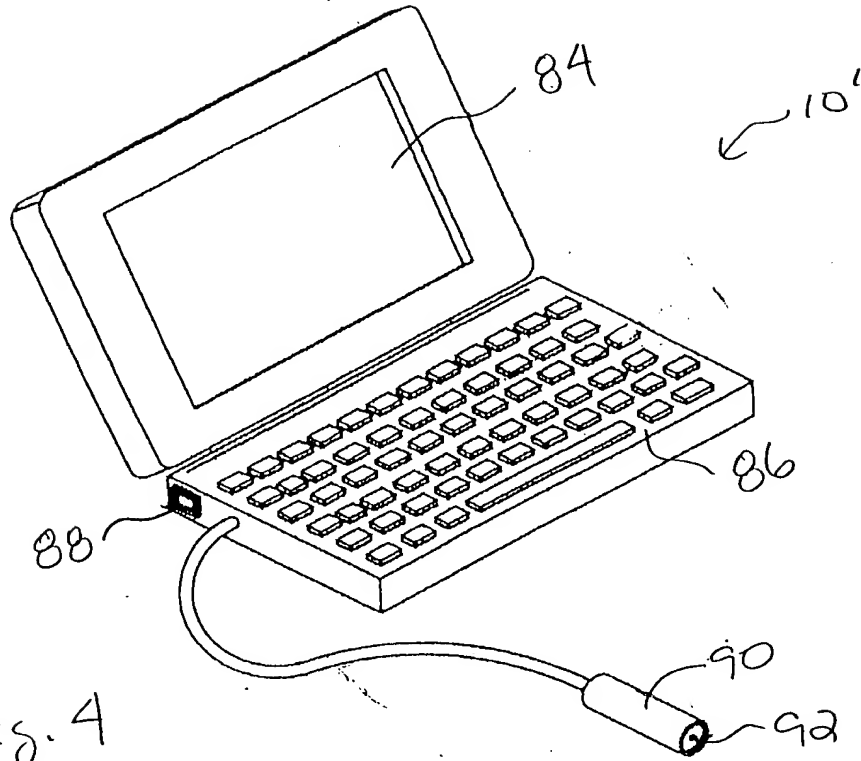


Fig. 4

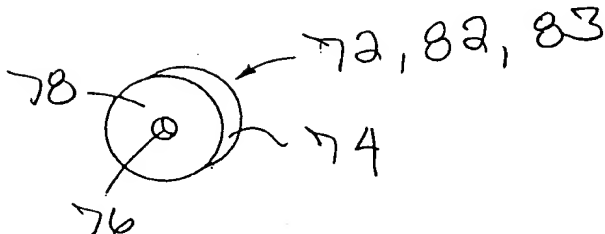


Fig. 5

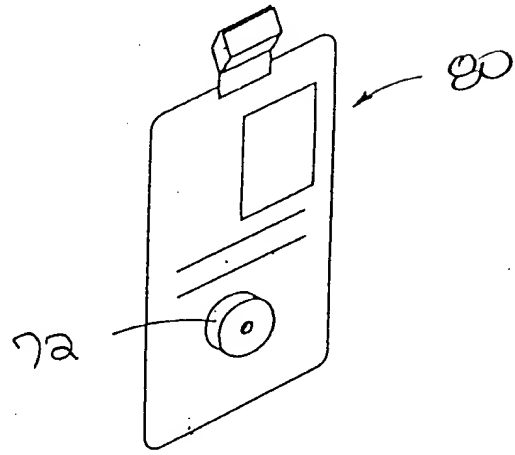


Fig. 6

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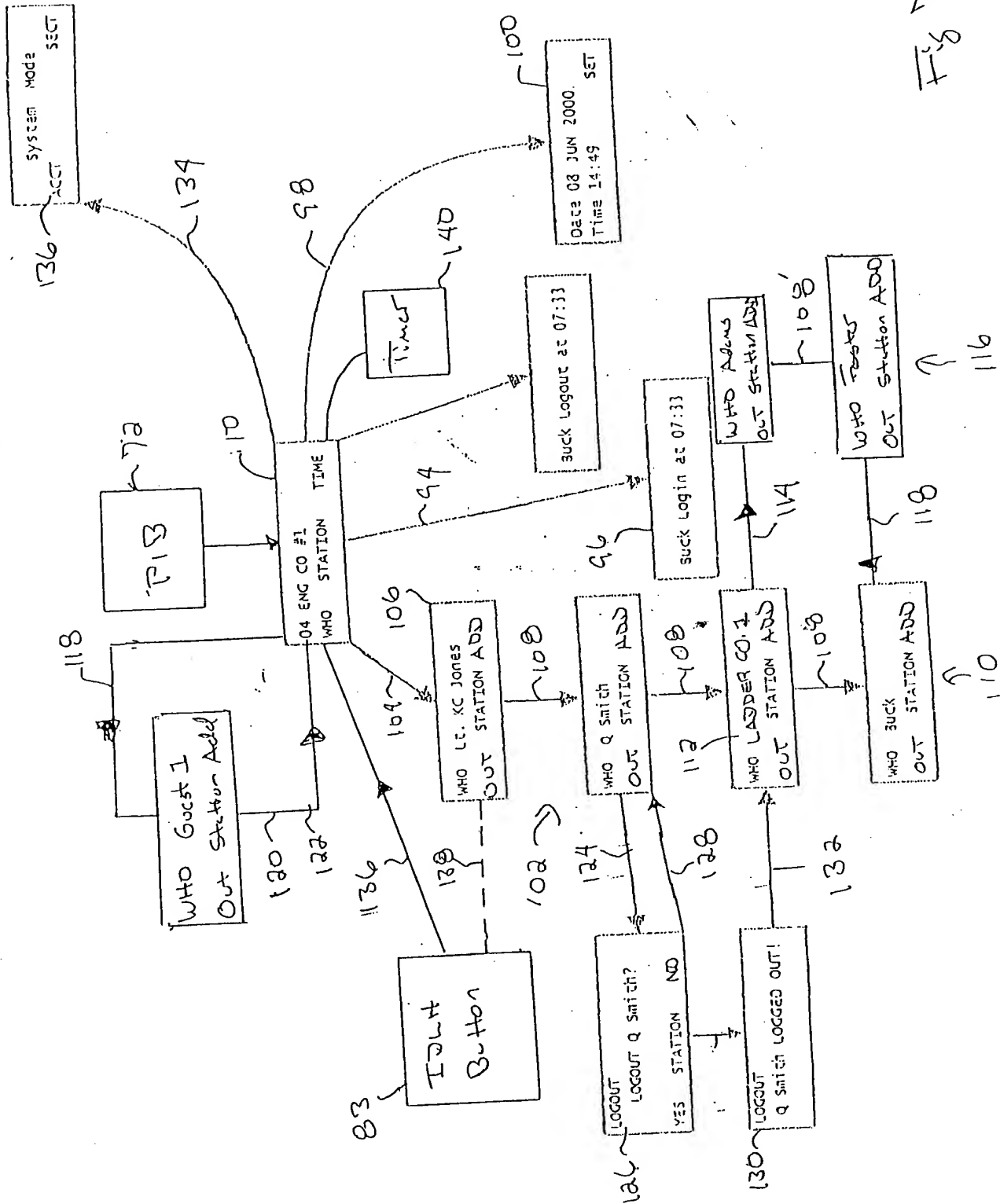
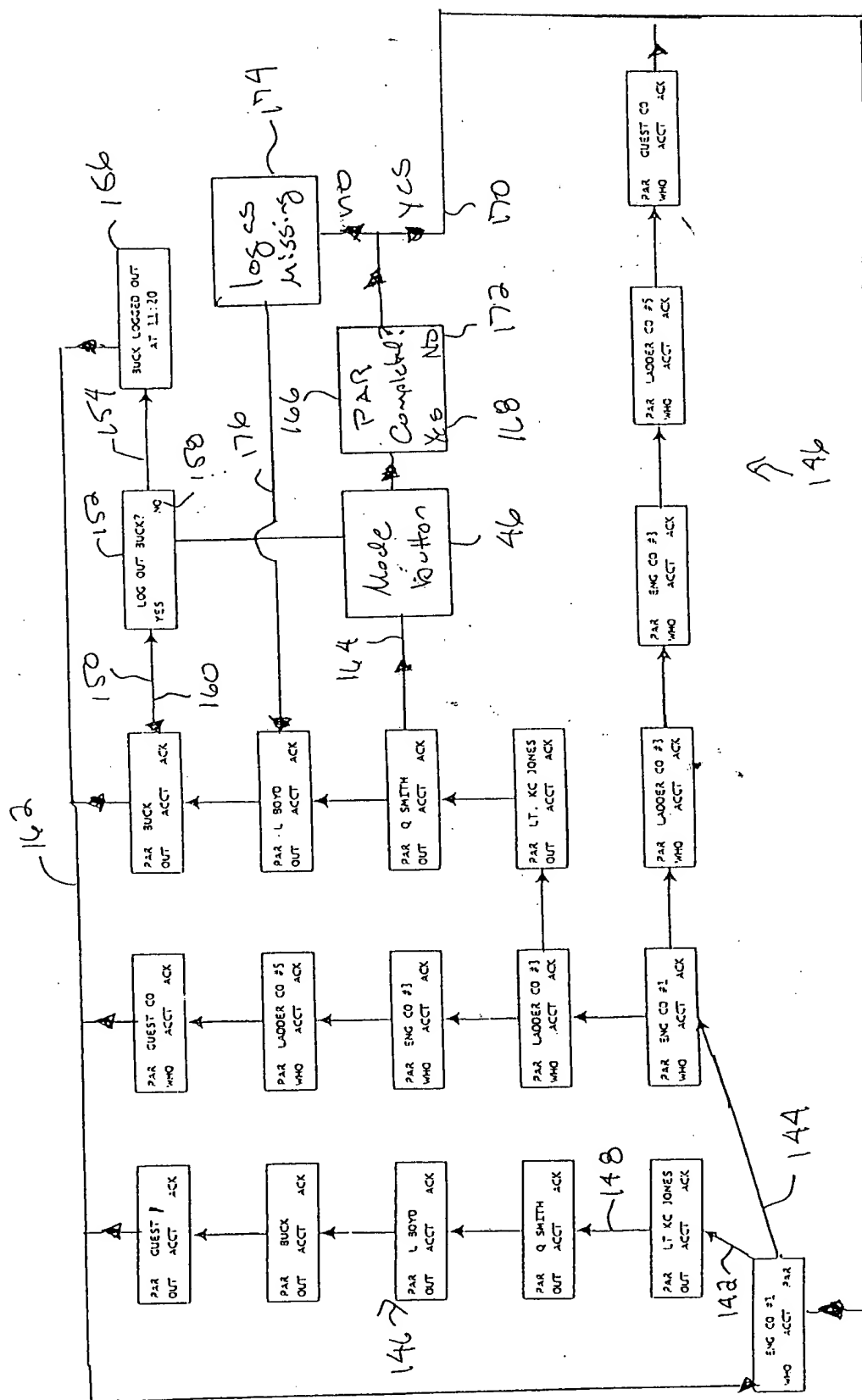


Fig. 7.



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FIG. 9 is a block diagram of the system architecture.

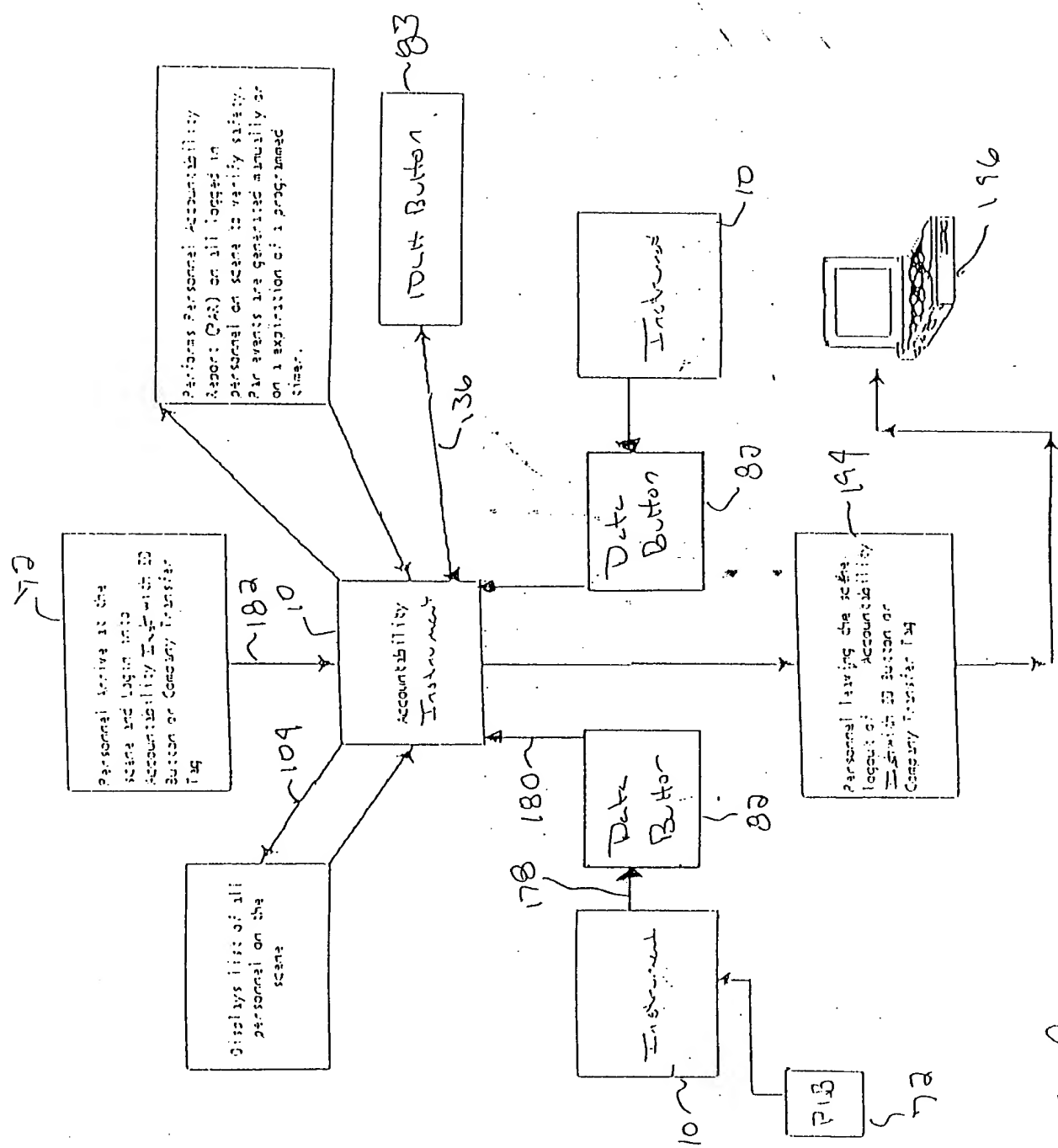
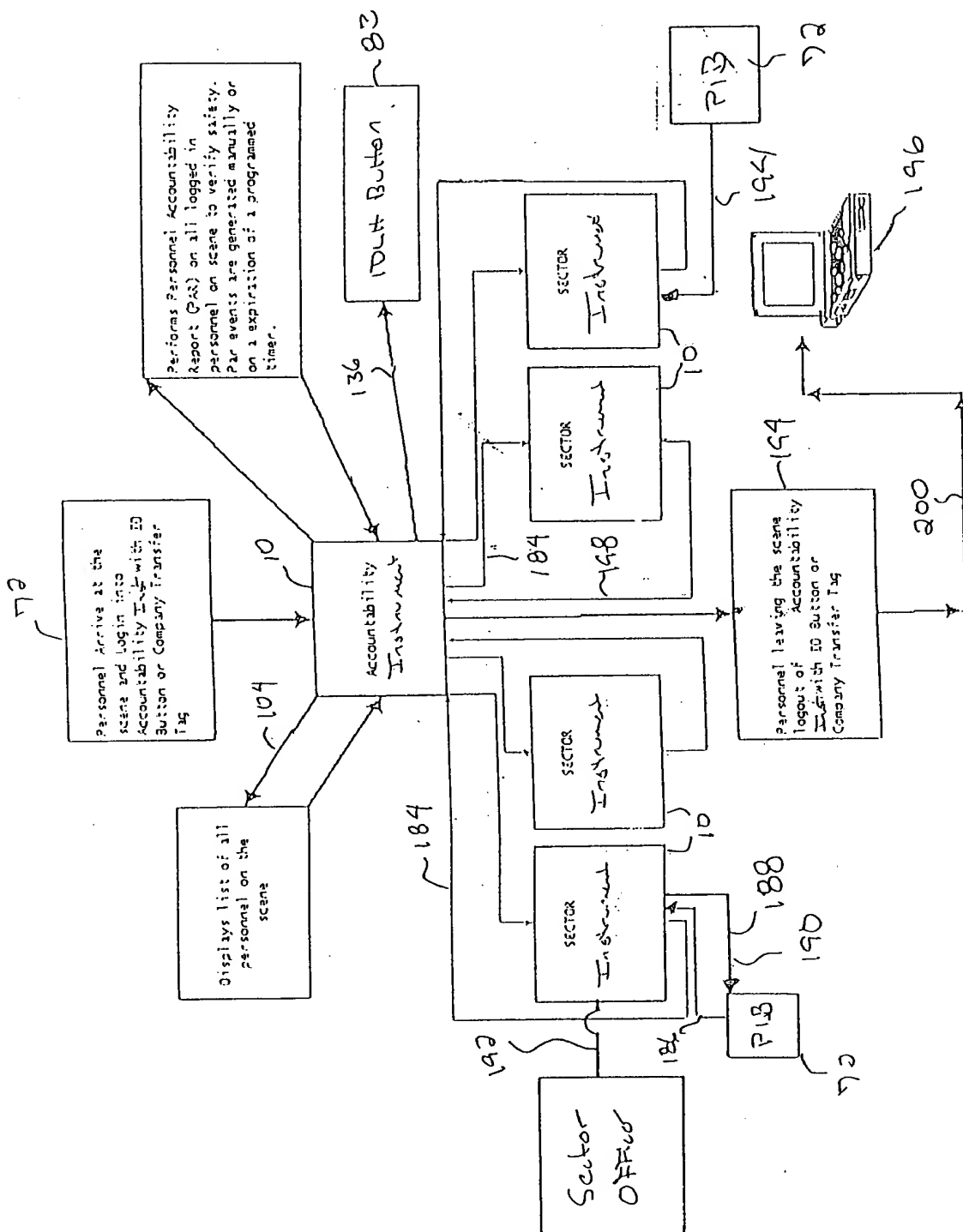


Fig. 9

[illegible]

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